

Freeform Search

Database:	<div style="border: 1px solid black; padding: 2px;"> US Pre-Grant Publication Full-Text Database US Patents Full-Text Database US OCR Full-Text Database EPO Abstracts Database JPO Abstracts Database Derwent World Patents Index IBM Technical Disclosure Bulletins </div>
Term:	<div style="border: 1px solid black; padding: 2px;"> l3 and bit\$1 </div>
Display:	<div style="border: 1px solid black; padding: 2px;"> 10 Documents in Display Format: <div style="border: 1px solid black; padding: 2px;">TI</div> Starting with Number <div style="border: 1px solid black; padding: 2px;">1</div> </div>
Generate: <input type="radio"/> Hit List <input checked="" type="radio"/> Hit Count <input type="radio"/> Side by Side <input type="radio"/> Image	

Search

Clear

Interrupt

Search History

DATE: Wednesday, April 28, 2004 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

Hit Count Set Name

result set

DB=PGPB; PLUR=YES; OP=OR

<u>L25</u> l3 and bit\$1 <u>L24</u> l3 and (mesi or moesi) <u>L23</u> l3 and ((state same bit\$1)) <u>L22</u> l3 and (hit-modif\$4 or (state with bit\$1))	1 <u>L25</u> 1 <u>L24</u> 1 <u>L23</u> 1 <u>L22</u>
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DB=PGPB,USPT; PLUR=YES; OP=OR

<u>L21</u> l3 and (leav\$3 or remain\$3)	1 <u>L21</u>
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DB=USPT; PLUR=YES; OP=OR

<u>L20</u> l1 and de-assert\$ <u>L19</u> l1 and biu <u>L18</u> l1 and miss\$3 <u>L17</u> L14 and ((miss\$3 same ((main or system) adj memory) adj3 controller)) <u>L16</u> L14 and ((miss\$3 same ((main or system) adj memory) with controller)) <u>L15</u> L14 and (miss\$3 same ((main or system) adj memory) same controller) <u>L14</u> l9 or 711/146.ccls. <u>L13</u> L9 and (miss\$3 same (main adj memory adj control\$4)) <u>L12</u> L9 and (miss\$3 with (main adj memory adj control\$4)) <u>L11</u> L9 and (main adj memory adj control\$4) <u>L10</u> L9 and (main adj3 memory adj3 control\$4) <u>L9</u> 711/141,143.ccls.	1 <u>L20</u> 1 <u>L19</u> 1 <u>L18</u> 22 <u>L17</u> 96 <u>L16</u> 135 <u>L15</u> 1198 <u>L14</u> 7 <u>L13</u> 2 <u>L12</u> 49 <u>L11</u> 128 <u>L10</u> 921 <u>L9</u>
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L8 11 and (main with memory)

1 L8

DB=PGPB; PLUR=YES; OP=OR

L7 L3 and 202

1 L7

L6 L3 and (controller)

1 L6

L5 L3 and (controller same (modified or unmodified))

1 L5

L4 L3 and unable

1 L4

L3 20030154352.pn.

1 L3

DB=USPT; PLUR=YES; OP=OR

L2 L1 and miss\$3

1 L2

L1 5765199.pn.

1 L1

END OF SEARCH HISTORY